

IN THE UNITED STATES PATENT OFFICE

In Re Patent of

Application No. 10/734,666

First Named Inventor: Srinivasan, Varadarajan

Confirmation No. 6261

Patent No.: 6,914,795

Issue Date: 7/5/2005

For: **CONTENT ADDRESSABLE MEMORY WITH
SELECTIVE ERROR LOGGING**

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Statement Under 37 CFR §3.73(b)

Sir:

NetLogic Microsystems, Inc., a Delaware corporation, hereby states that it is the assignee of the entire right, title and interest in the above-identified patent by virtue of the Assignments that are recorded in the USPTO at Reel/Frame **012796/0130**.

The undersigned is authorized to act on behalf of the assignee at least for purposes of making the foregoing statement.

Effective immediately, please direct all further communications in the above-identified patent application to the following address:

**Mahamedи Paradice Kreisman LLP
550 South Winchester Blvd., Suite 605
San Jose, CA 95128**

**Telephone: (408) 236-6640
Facsimile: (408) 236-6641
Customer No. 93922**

Respectfully submitted,
MAHAMEDи PARADICE KREISMAN LLP

Date February 11, 2010

/William L. Paradice III, Reg. # 38,990/
William L. Paradice III, Reg. # 38,990
Tel. No. 408-236-6646